

**PATENT**  
**IBM Docket No. RAL920000070US1**

**Amendments to the Specification:**

On page 5, please amend paragraph beginning on line 15 as follows:

A1  
The interface provides for the automatic generation of the DUT configuration file syntax from the bus architecture file syntax. The syntax includes parameter combination specifications and rules. The parameter combination specifications are used by the generator to systematically generate or enumerate possible bus transactions for output to a test case, and the rules provide for the exclusion of specified parameter combinations from the test case to customize the generated parameter combinations to a particular DUT. The parameterized bus transactions may be used to test compliance of the DUT with the bus architecture.

Serial Number 09/638,757

- 2 -